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PATENT ABSTRACTS OF JAPAN

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H04N 5/66(21) Application number: **05347576**(22) Date of filing: **24 . 12 . 93**(71) Applicant: **TEXAS INSTR INC <TI> SONY CORP**(72) Inventor: **ASHIZAKI KOJI**(54) **GRADATION REPRESENTING METHOD**

avoided effectively.

(57) Abstract:

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PURPOSE: To surely represent multiple gradations while effectively avoiding the occurrence of flickers at the time of representing the gradations using an on/off type spatial optical modulating element by using a light source whose intensity can be modulated and representing the gradations by modulating the intensity of the light from the light source in accordance with the gradations at each pixel of picture data.

CONSTITUTION: Picture data S3 stored in a video RAM 4 are read into a very small specular element 2 at every bit frame in accordance with a write control signal from a timing control circuit 5, and each pixel of the element 2 is turned on or off in accordance with picture data S8. Simultaneously, a signal S7 which determines the intensity of the light emitted from a light source 6 is sent from the control circuit 5 through a light intensity modulating circuit 7. Consequently, the brightness of each pixel of the element 2 is determined by the sum total of the intensity of the light when the pixel is in a turned-on state during the period of a certain frame. Thus, the transferring time of one frame can be minimized and the occurrence of flickers can be

